

Figure 1b

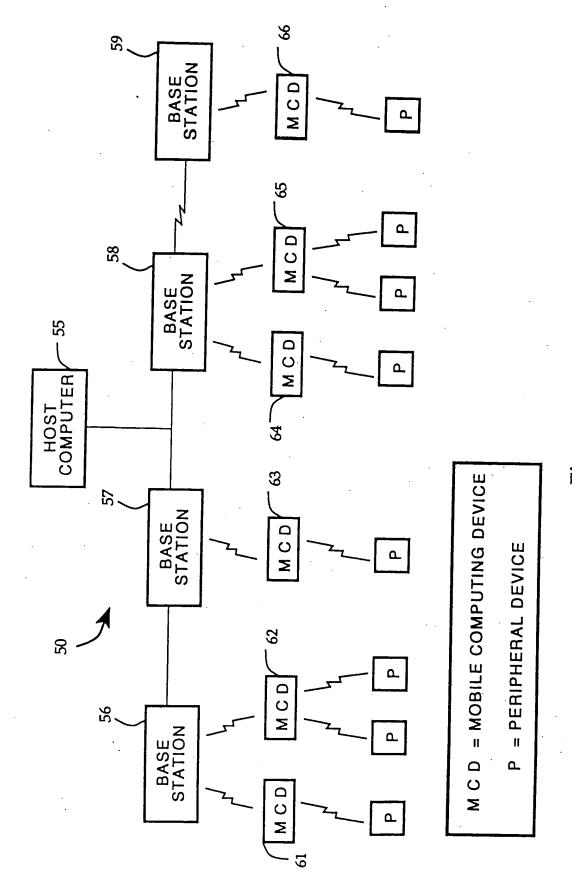


Figure 1c

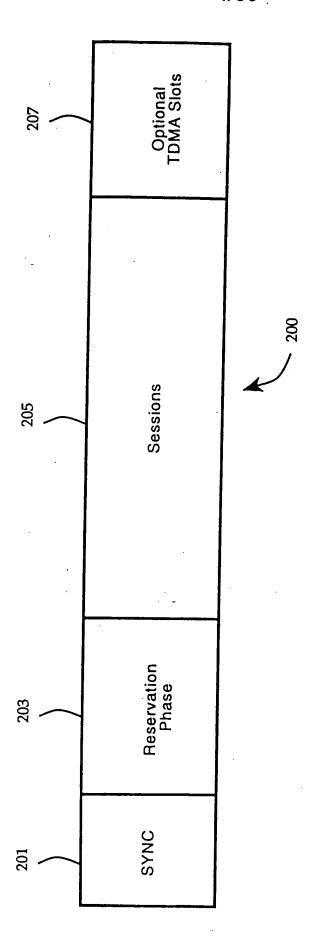


Figure 2

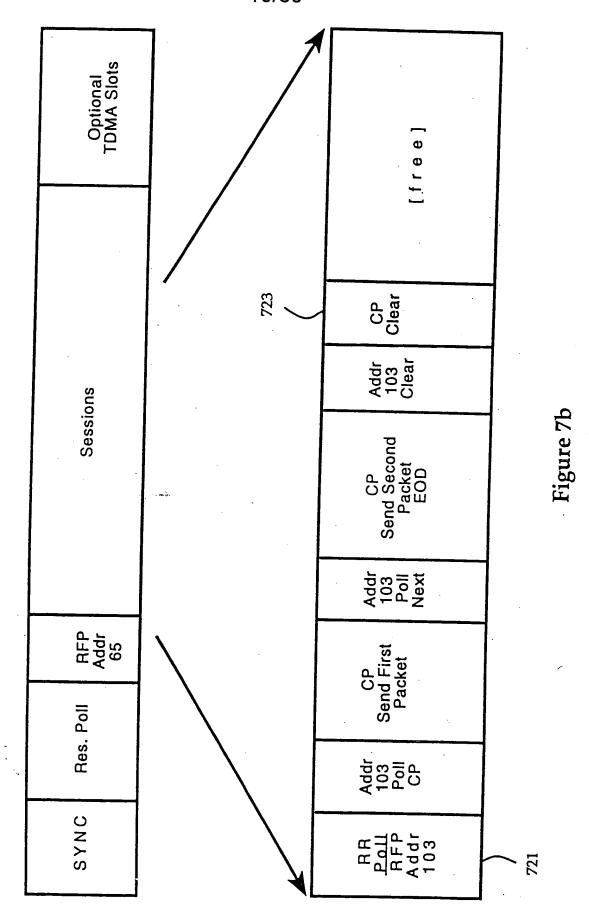
10 ms	Freq. 1 2466 MHz	301		20 ms	Freq. 1 2466 MHz	303	
10 ms	Freq. 79 2403 MHz	301		20 ms	Freq. 79 2403 MHz	303	
10 ms				20 ms			
10 ms	Freq. 5 2444 MHz	301	e 3a	20 ms	Freq. 5 2444 MHz	303	35
10 ms	Freq. 4 2472 MHz	301	Figure 3a	20 ms	Freq. 4 2472 MHz	303	Figure 3b
10 ms	Freq. 3 2417 MHz	301		20 ms	Freq. 3 2417 MHz	303	
10 ms	Freq. 2 2411 MHz	301		20 ms	Freq. 2 2411 MHz	303	
10 ms	Freq. 1 2466 MHz	301		20 ms	Freq. 1 2466 MHz	303	

Al. 1 20 ms	Al. 2 A 20 ms 20 Figure 4a	Al. 1 20 ms e 4a	AI. 2 20 ms	Al. 1 20 ms	AI. 2 20 ms
	Figur	e 4a			
	Figur	e 4a			
Freq 1: 2466 MHz			Freq 2:	2411 MHz	
AI. 3	Al. 4	Al. 1	AI. 2	AI. 3	AI. 4
20 ms	20 ms	20 ms	20 ms	20 ms	20 ms
		·		:	
1 2 1	4Hz Al. 3 20 ms	3 Al.	3 Al. 4 ns 20 ms Figure 4	3 Al. 4 Al. 1 A al. 1 A scores 20 ms 20 ms Eigure 4b	Freq 2: 24 3 Al. 4 Al. 1 Al. 2 ns 20 ms 20 ms Figure 4b

	Optional TDMA Slots			Optional TDMA Slots
	Sessions	Figure 5a		Sessions
501	Slot 1		503	RFP Addr 65
	Res. Poll			Res. Poll
	SYNC			SYNC

Figure 5b

		1		7
	Optional TDMA Slots		Optional TDMA Slots	
	Sessions		Sessions	
601	Slot	Figure 6a 5	Slot	, Figure 6b
		Figu	RFF Addr 111 RFF Addr 02) 607 Figu
601	Slot 2		Slot 2	
601	Slot 1	603	RFP Addr 19	
	Res. Poll	·	Res. Poll	
·	SYNC		. O ≻ ⊗	
•	, i de la constant	•		



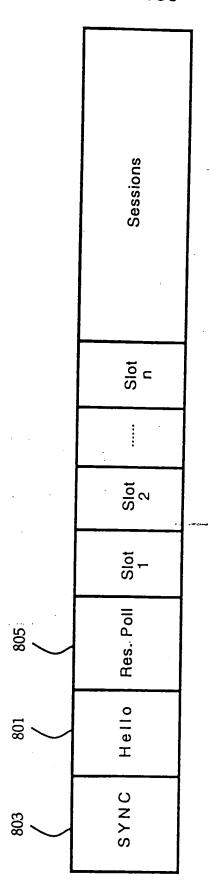


Figure 8

907

903

206





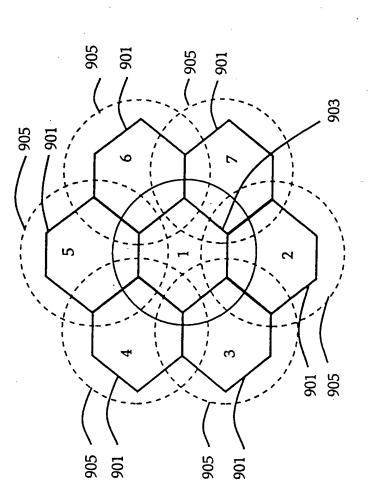
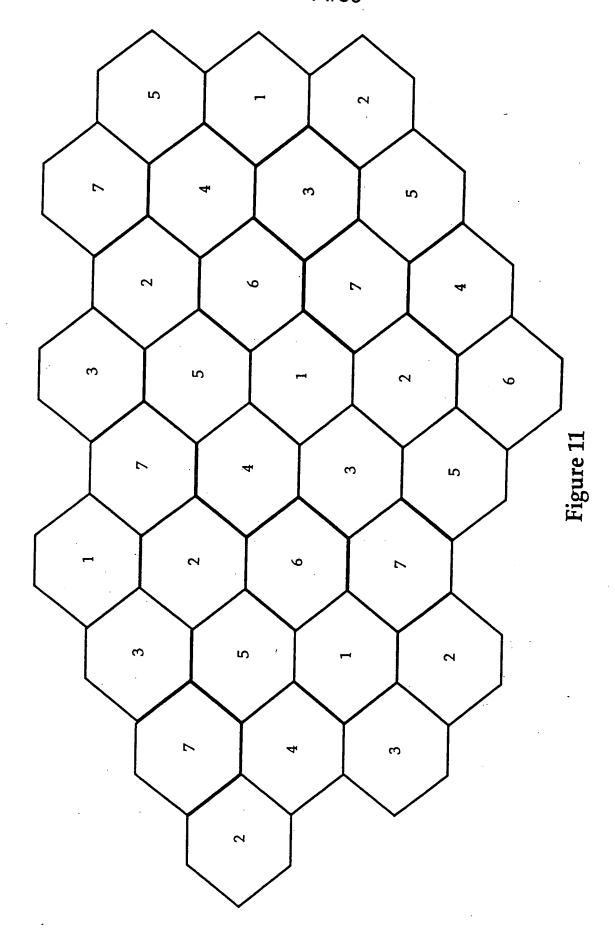


Figure 9a



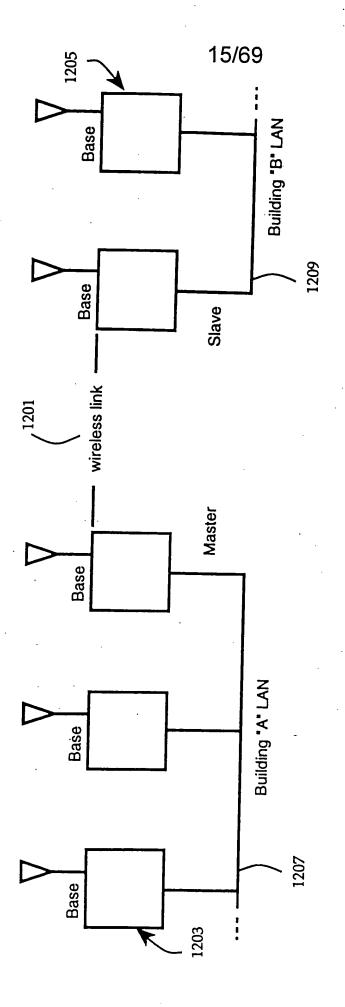


Figure 12

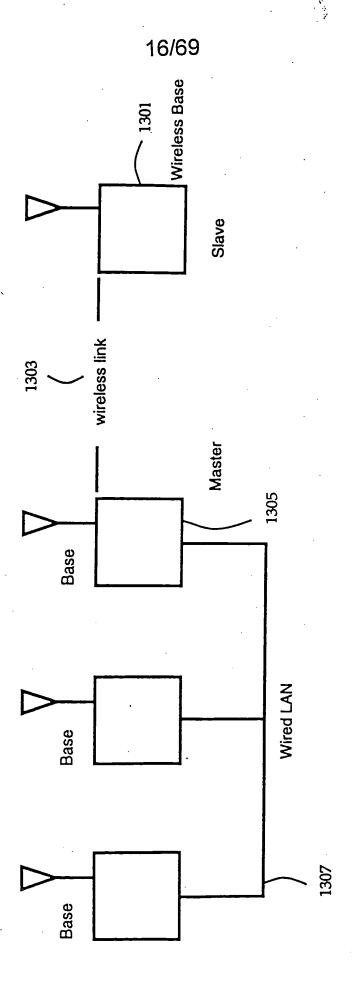


Figure 13

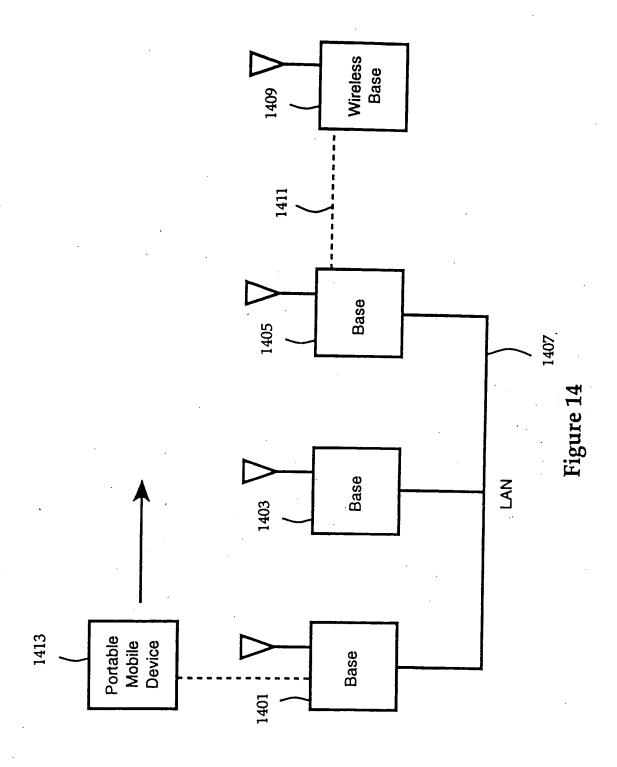


Figure 15

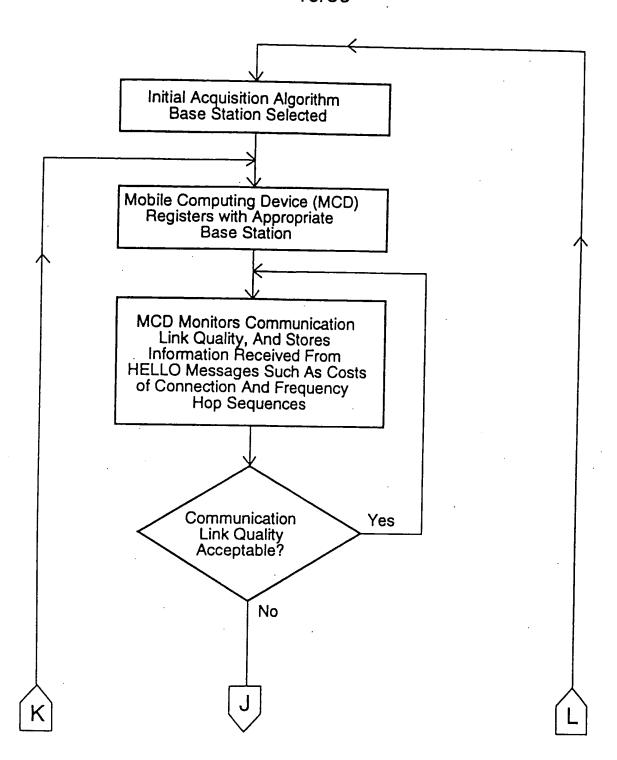
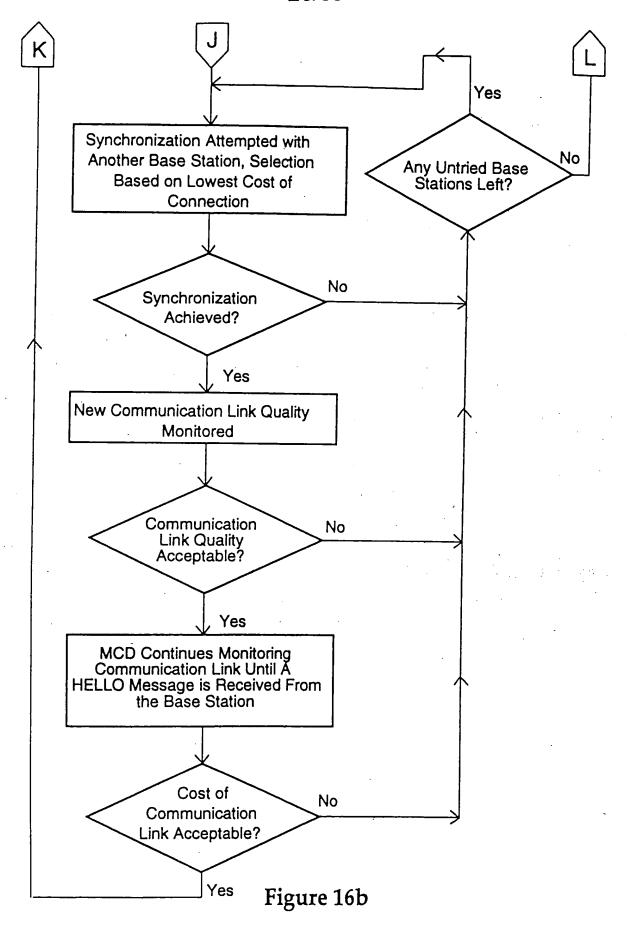


Figure 16a



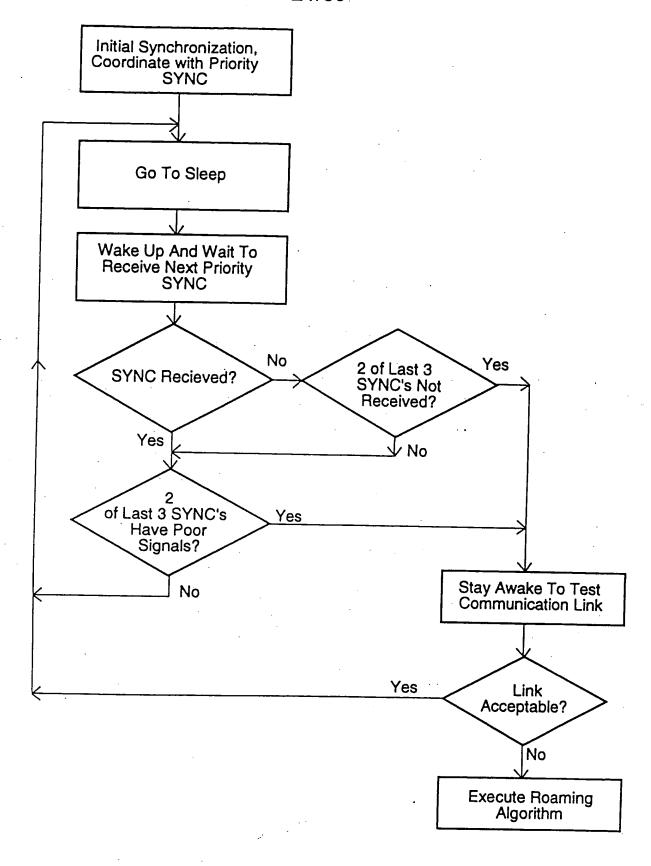
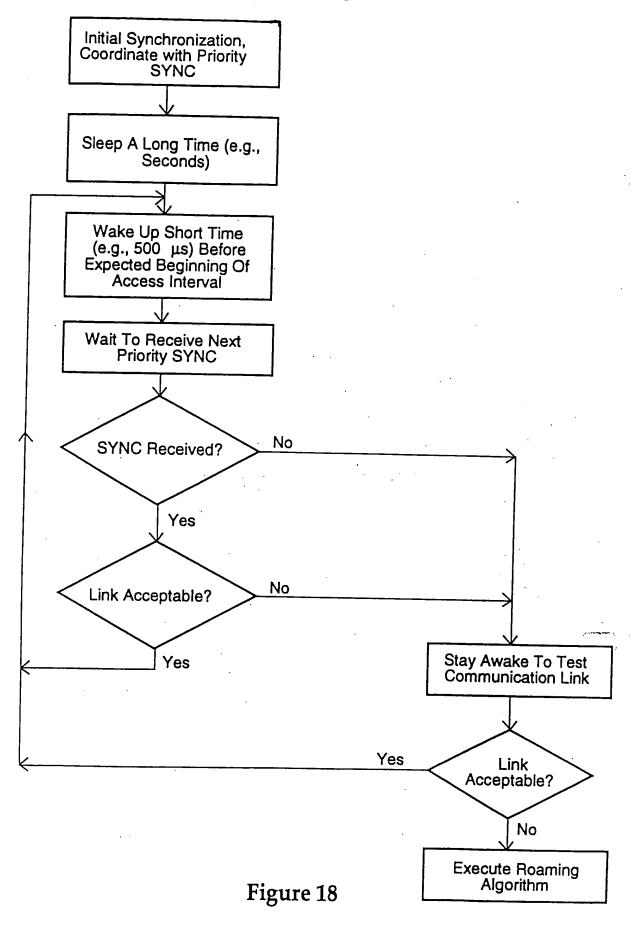
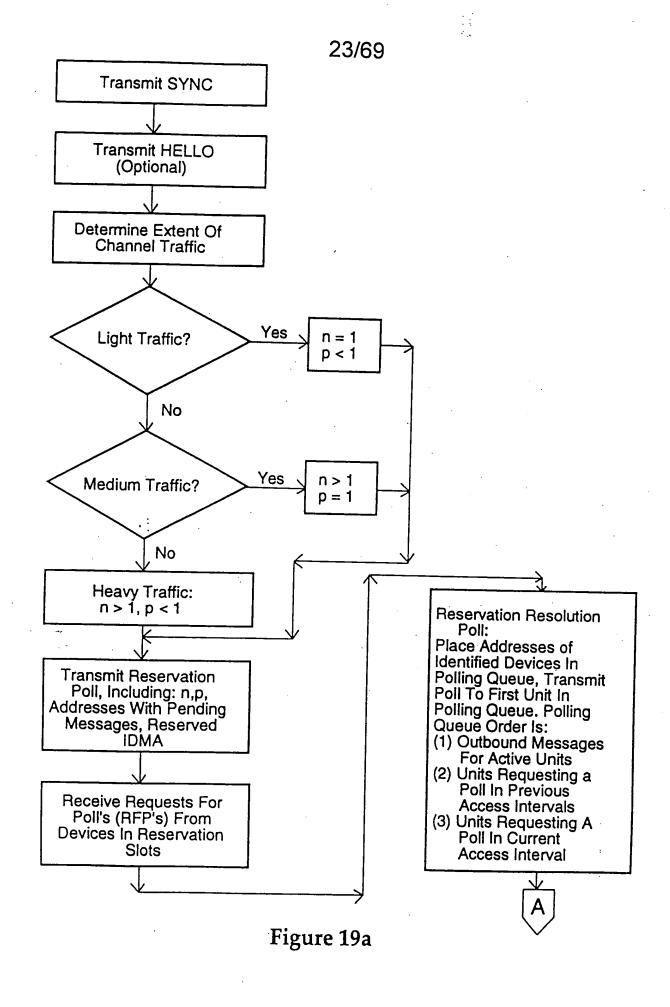


Figure 17





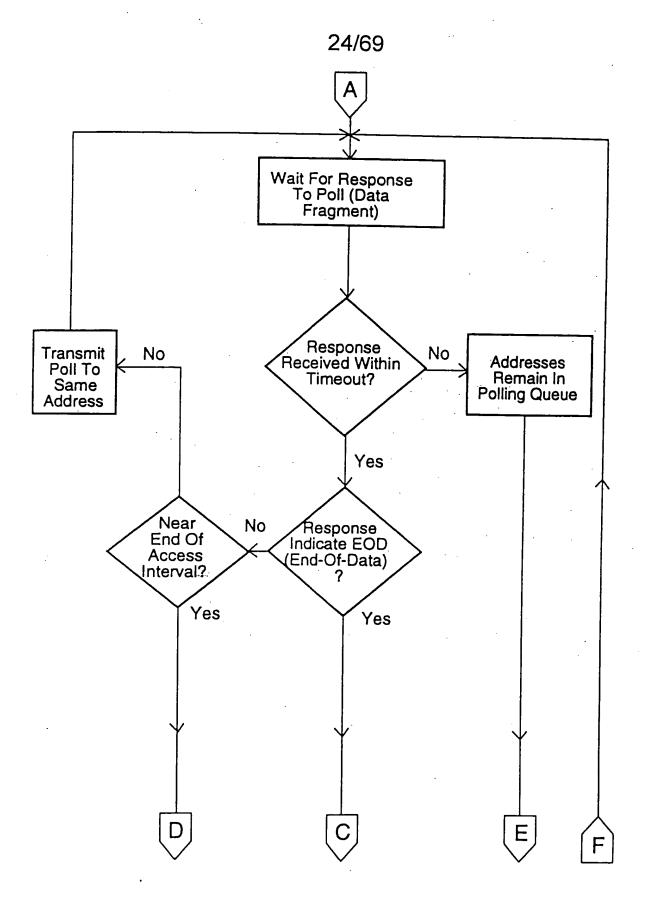


Figure 19b-1

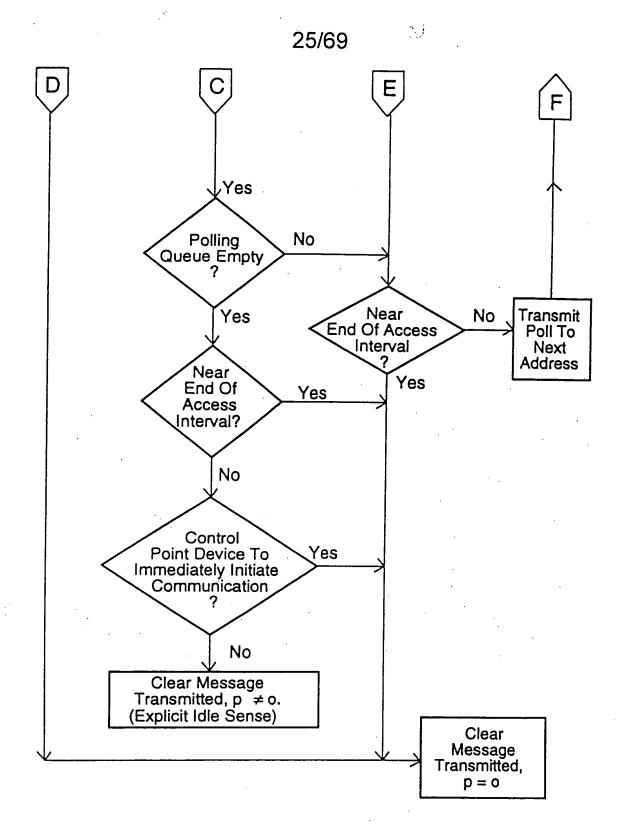
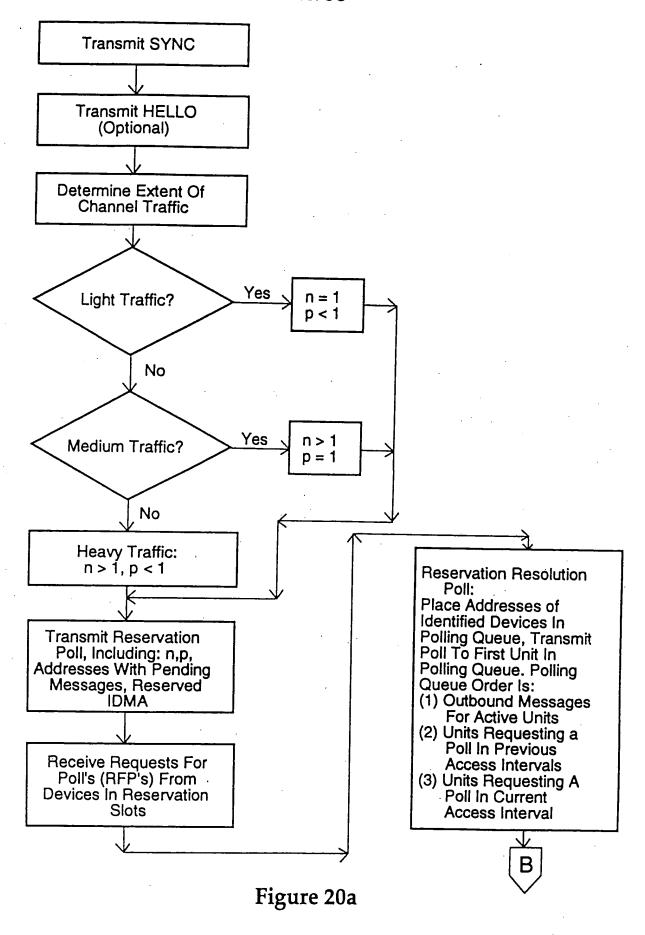


Figure 19b-2



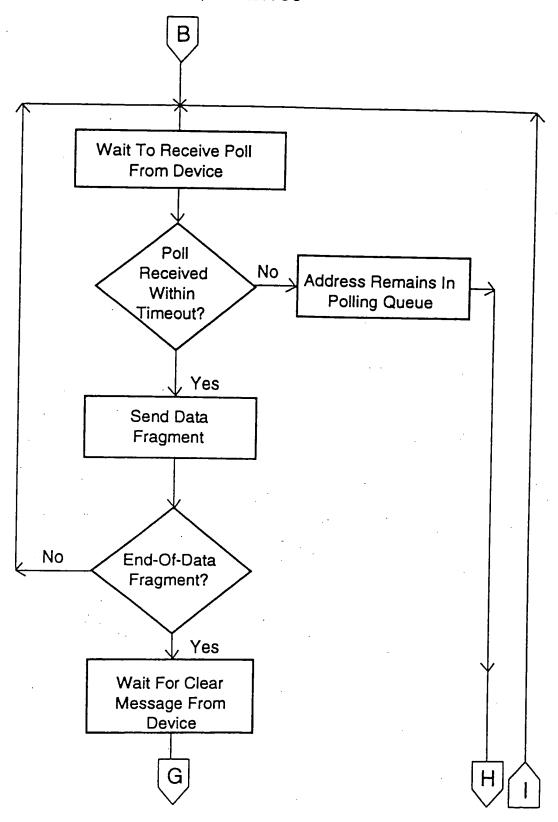


Figure 20b-1

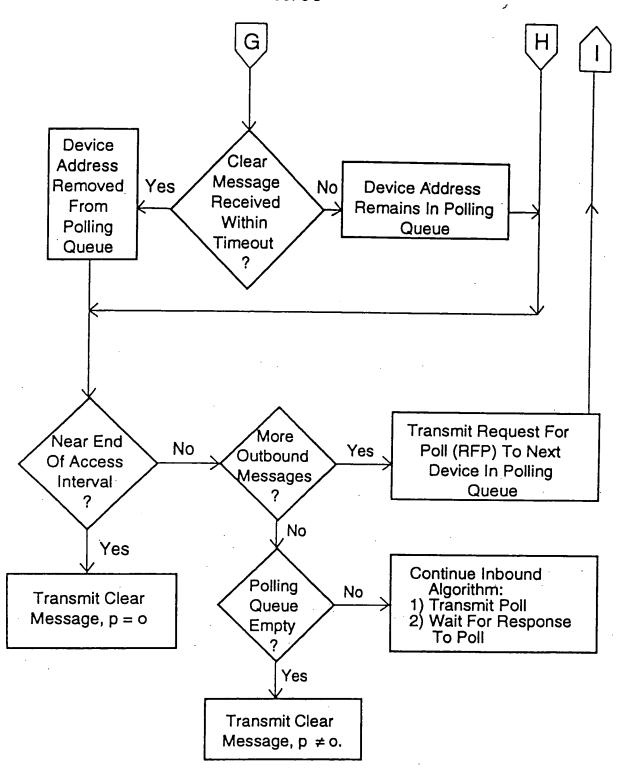


Figure 20b-2

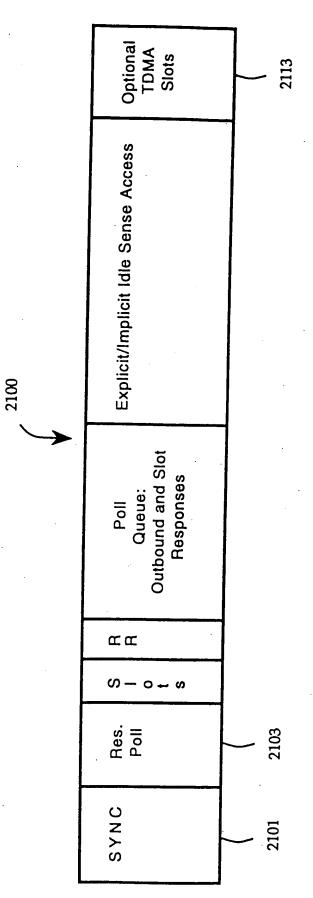


Figure 21

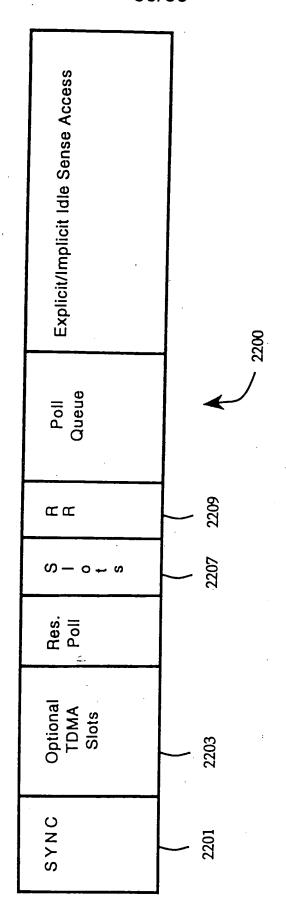


Figure 22

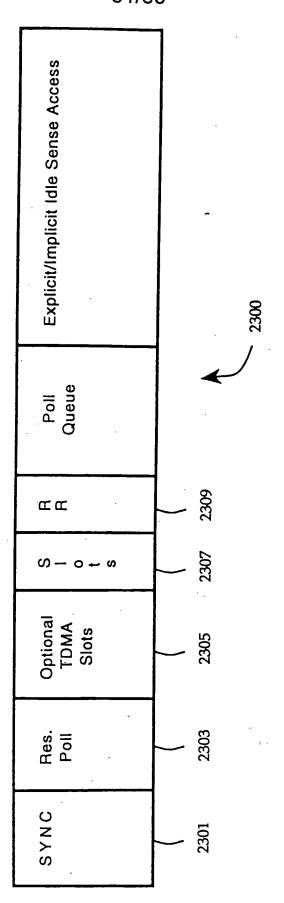
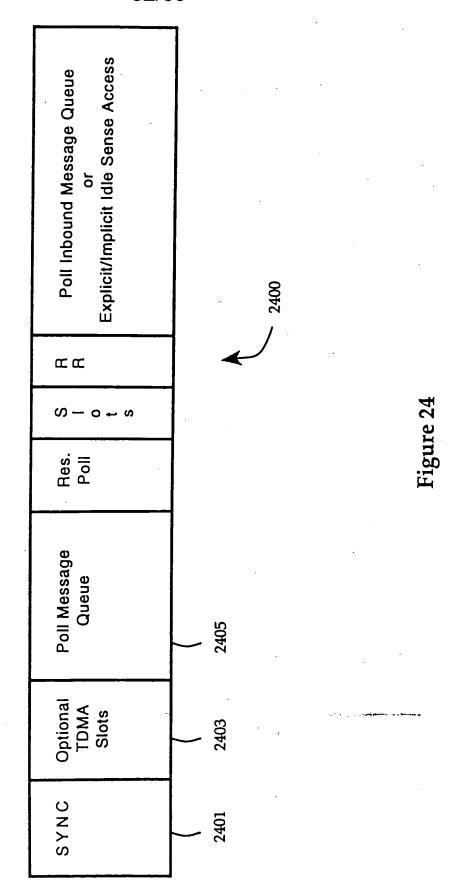
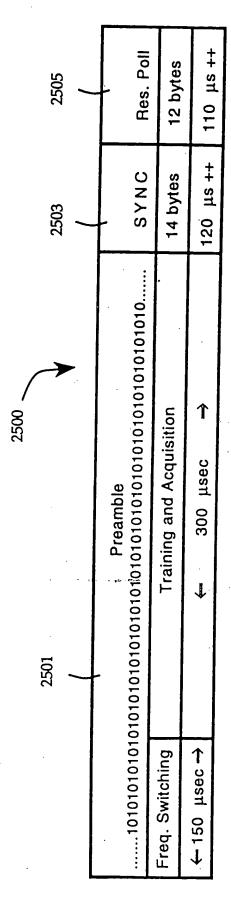


Figure 23





++ allows additional bit times for HDLC zero bit insertion

Figure 25

		SYNC \$700
Byte	Content	Definition
1	FLAG	Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _H)
3	Dest. Address 2	Destination Address, 2nd Byte
4	ТҮРЕ	Message Type=SYNC, Priority Sync, or Priority SYNC with
-		HELLO
5	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
8*	NET ID	Net ID
*6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10*	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Slots
		3 Bits Number of Available Time Slots
11*	Random	Randomly Selected Character for Improved Engages
12	CRC 1	Error Check
13	CRC 2	Frror Check
14	FLAG	Frame Delimiter

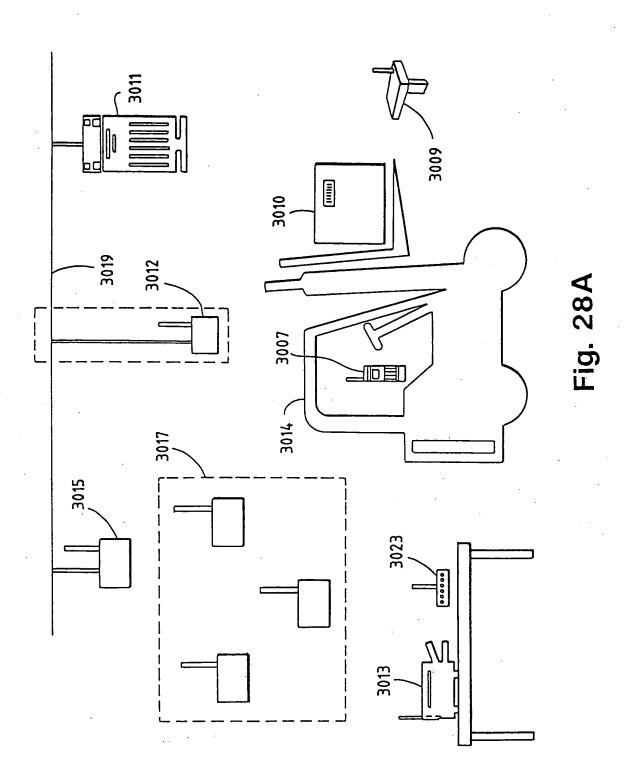
Figure 26

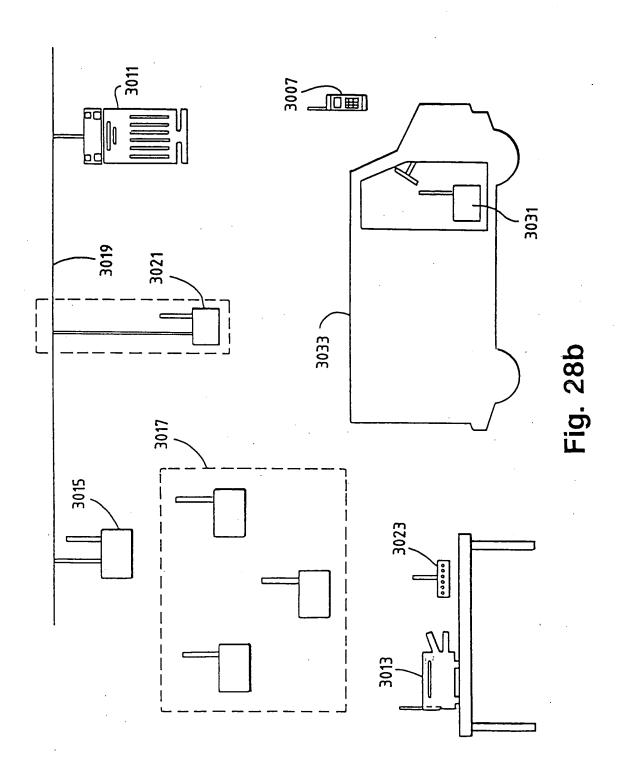
*Optionally Encrypted Bytes

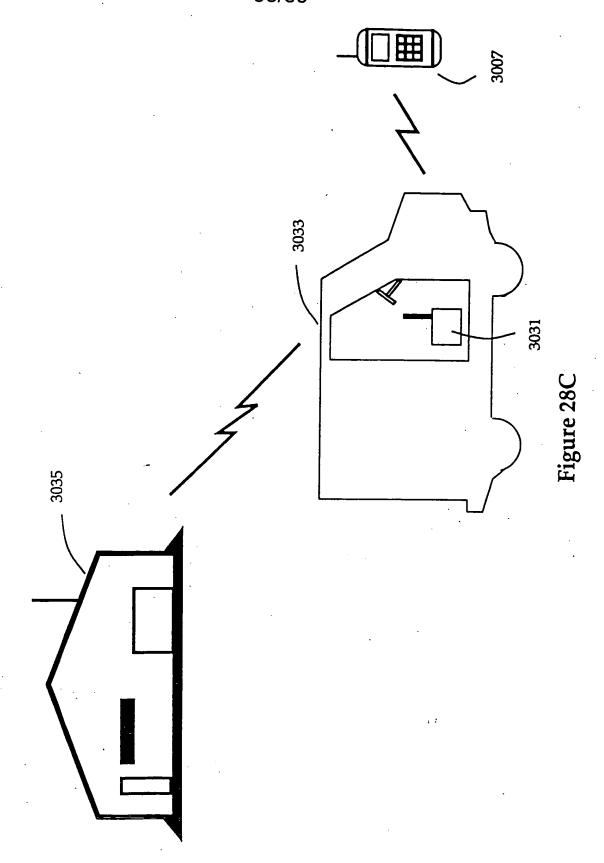
Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFFF)	Destination Address, 2nd Byte	Message Type = RES POLI	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Segment ID	The Company of Ariang	Xabiii adiinhac Biiiddoi	3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved			EITOI Check	Frame Delimiter
Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	SLOT PARMS		Sequence	Index	NET PARMS				CRC 1	CRC 2	FLAG	· ·
Byte	1	2	3	4	5	9	7*	8*	*6				10	11	12	

Reservation Poll

Figure 27







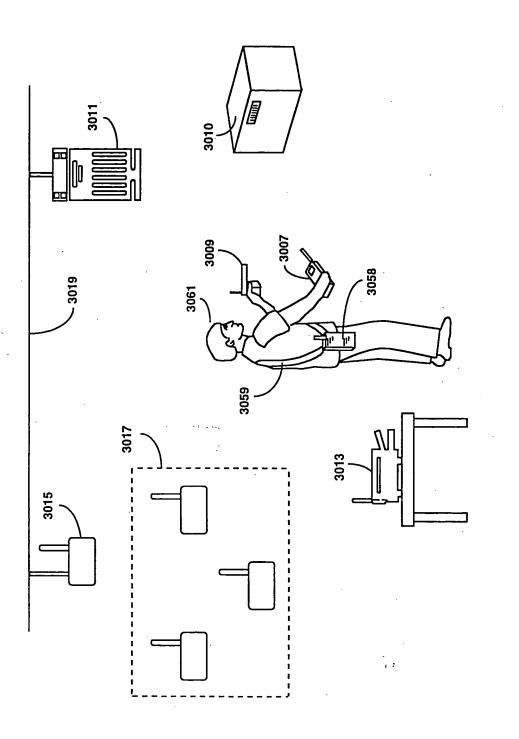


Fig. 298

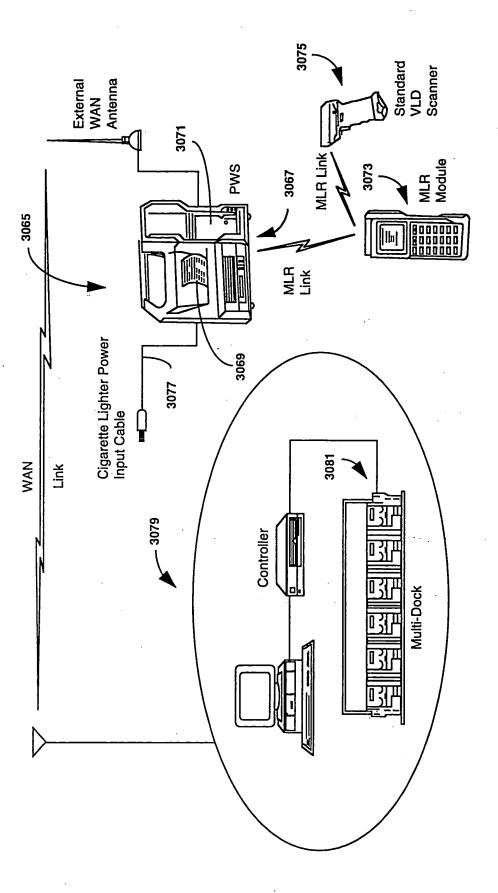
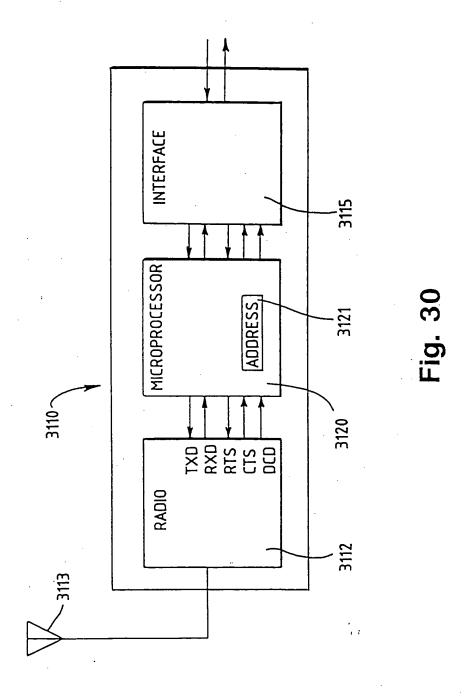


Fig. 29b



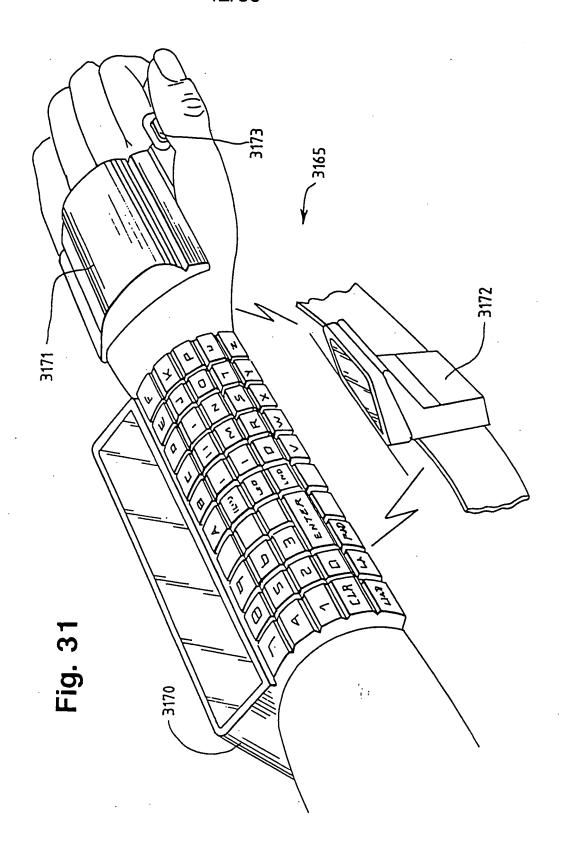
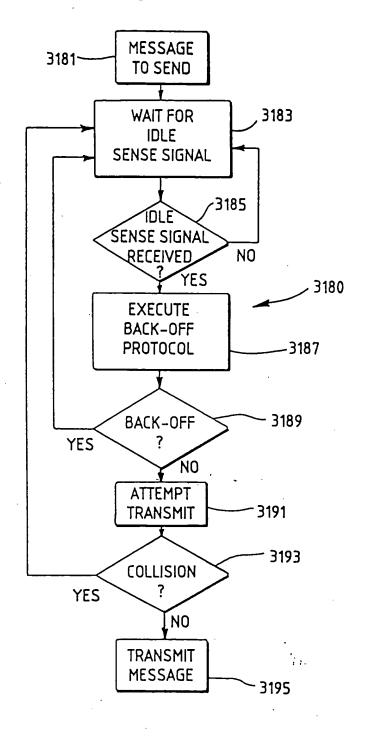


Fig. 32



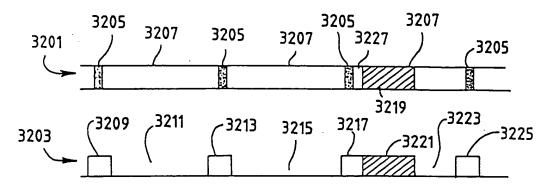


Fig. 33a

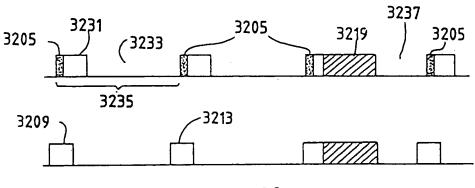
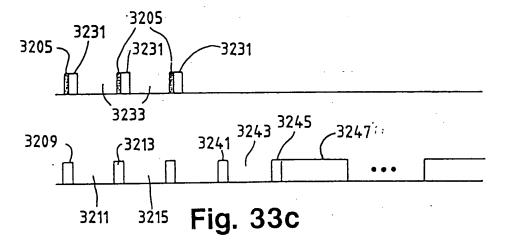
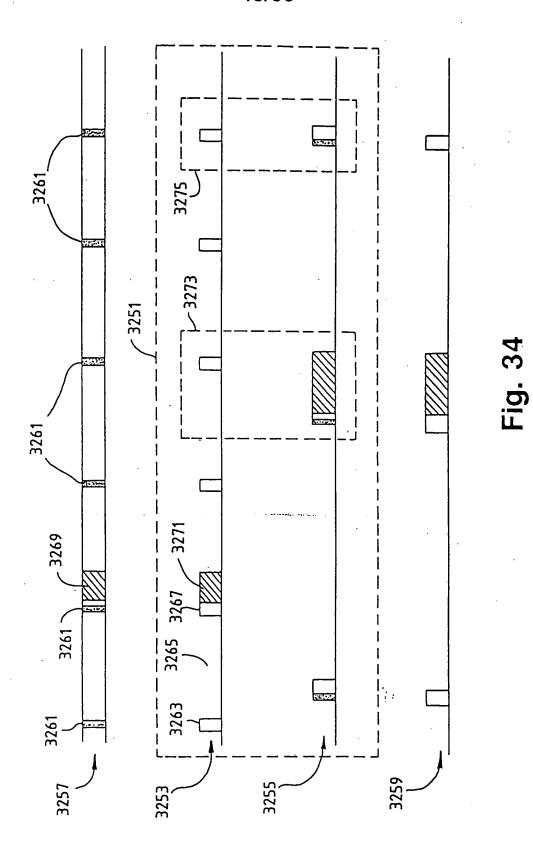


Fig. 33b





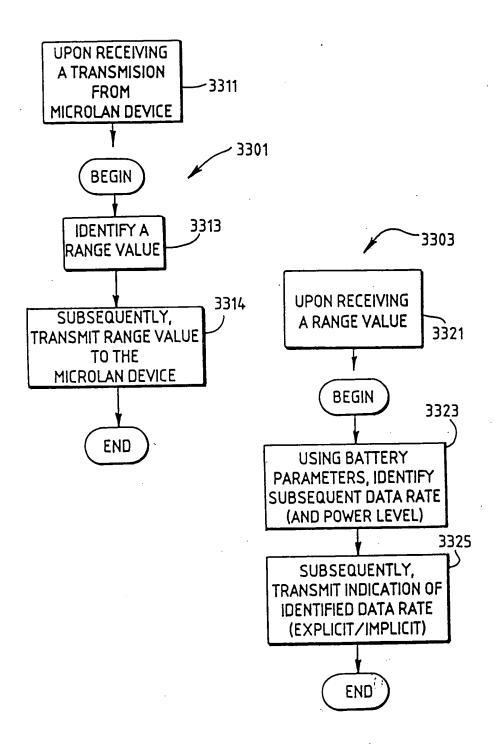
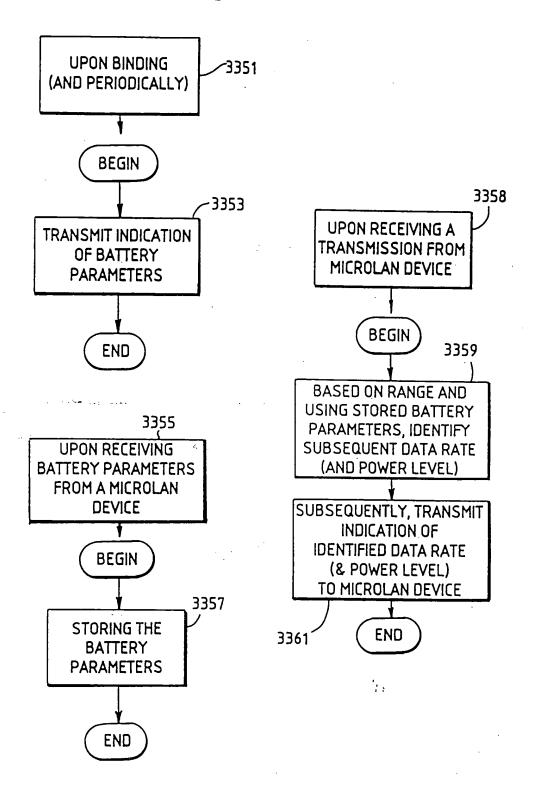


Fig. 35

Fig. 36



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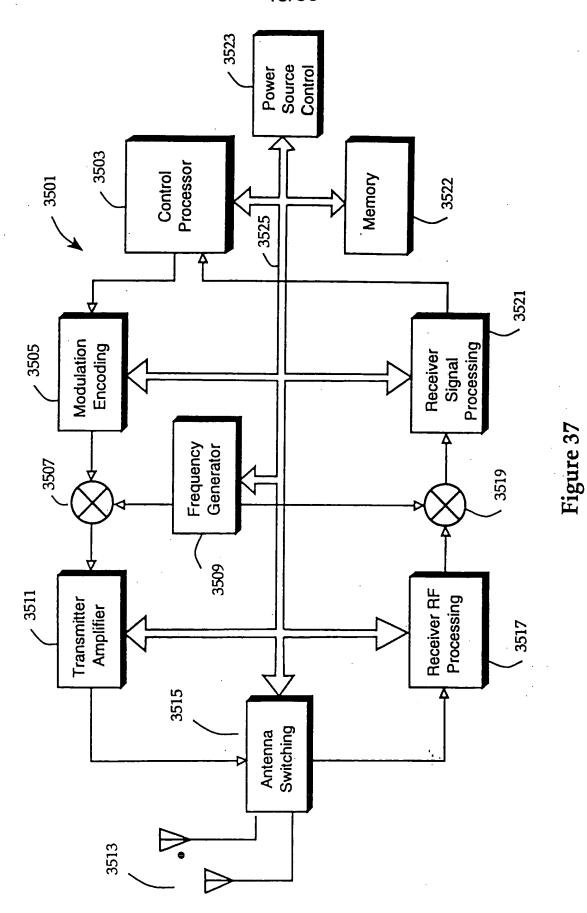


Figure 38

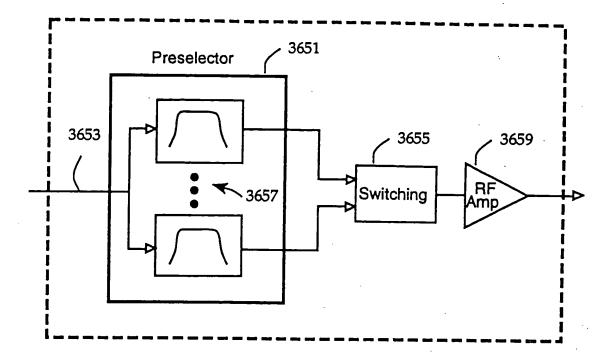


Figure 39

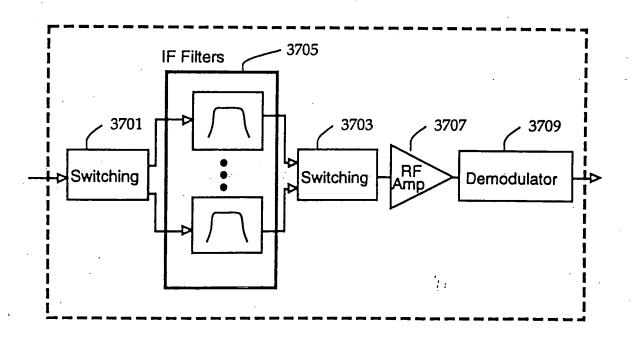
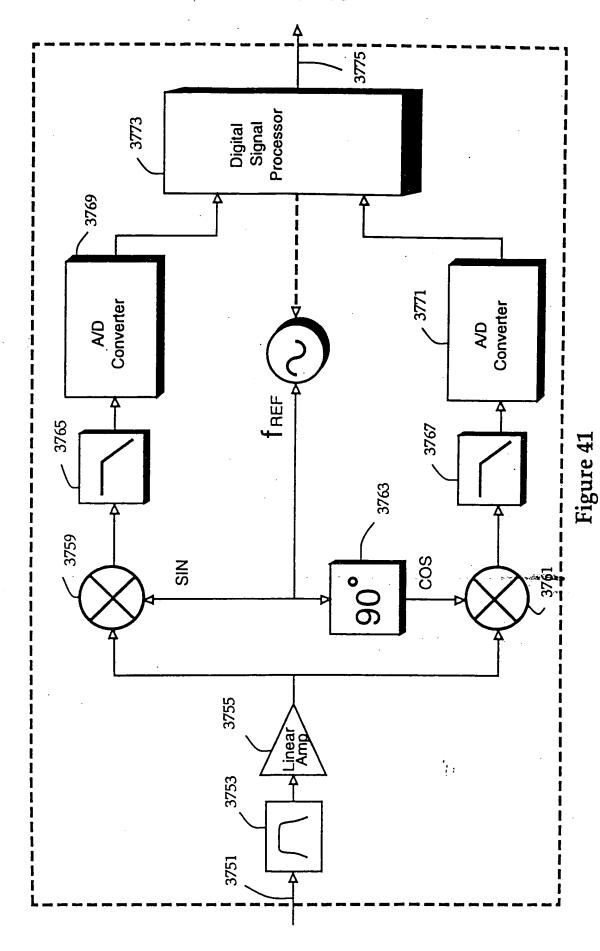


Figure 40



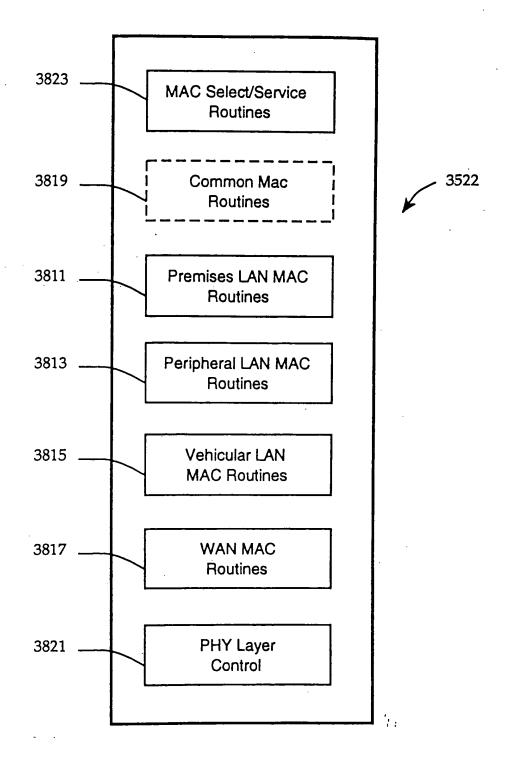
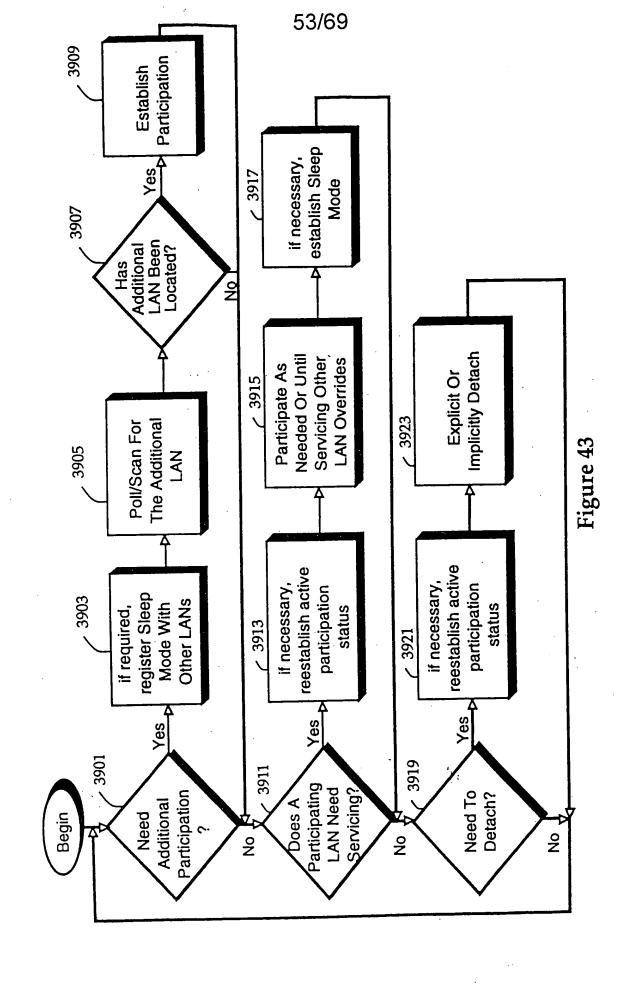


Figure 42



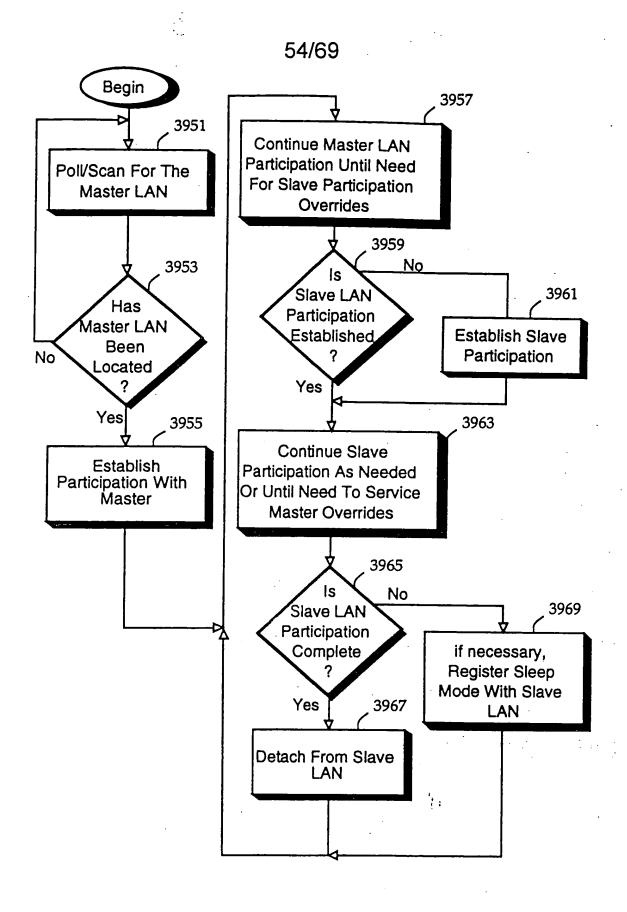


Figure 44

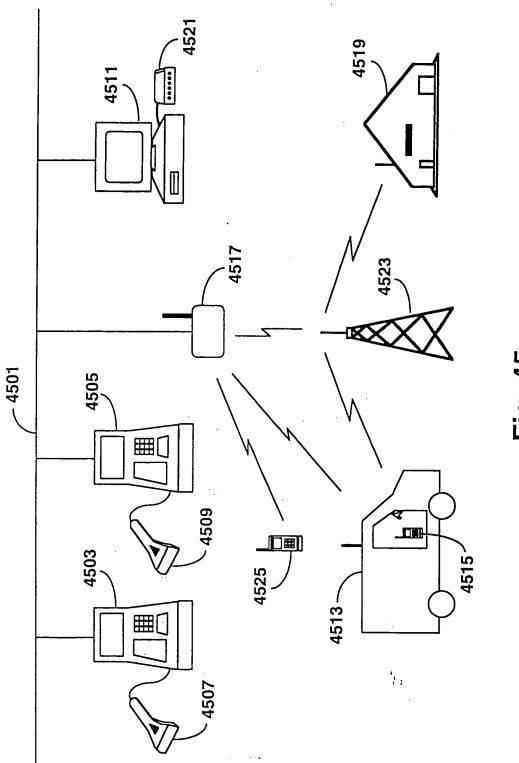


Fig. 45

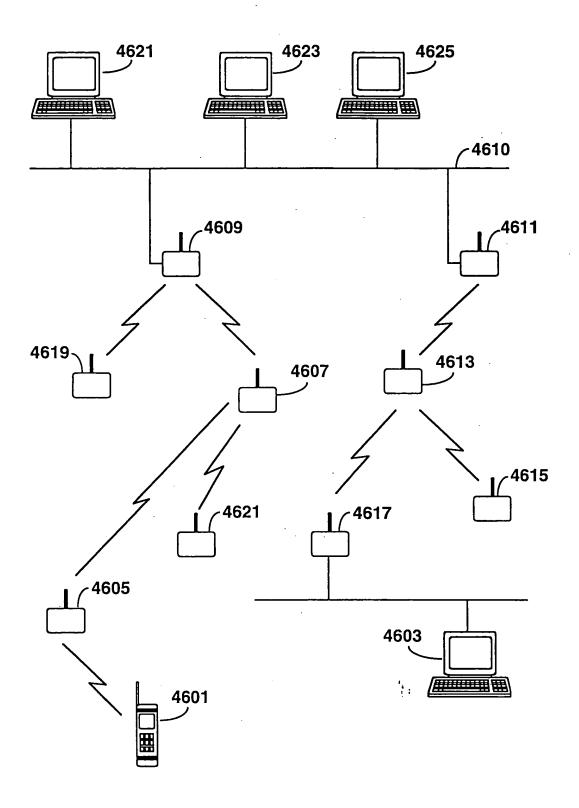


Fig. 46a

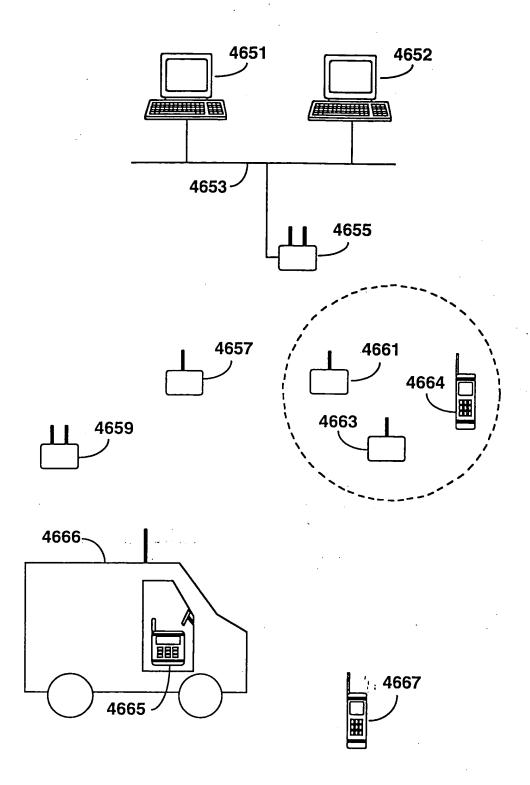


Fig. 46b

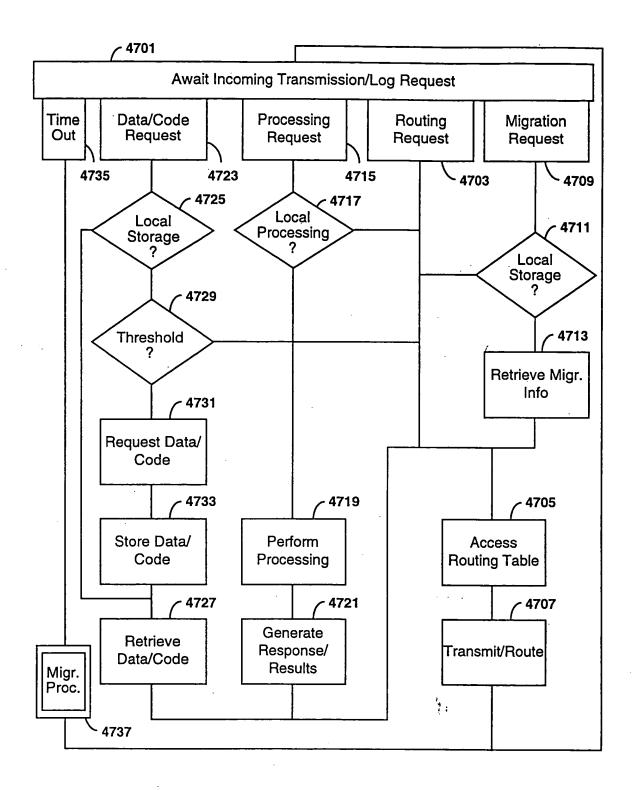


Fig. 47a

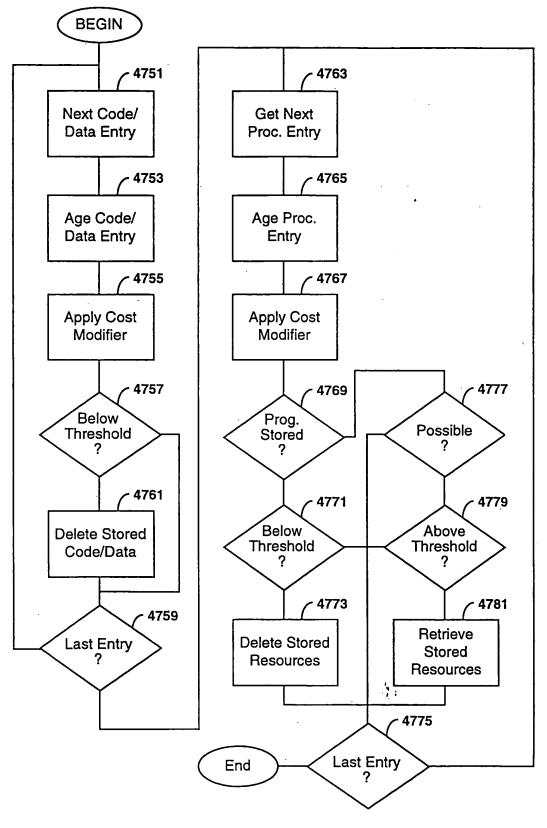


Fig. 47b

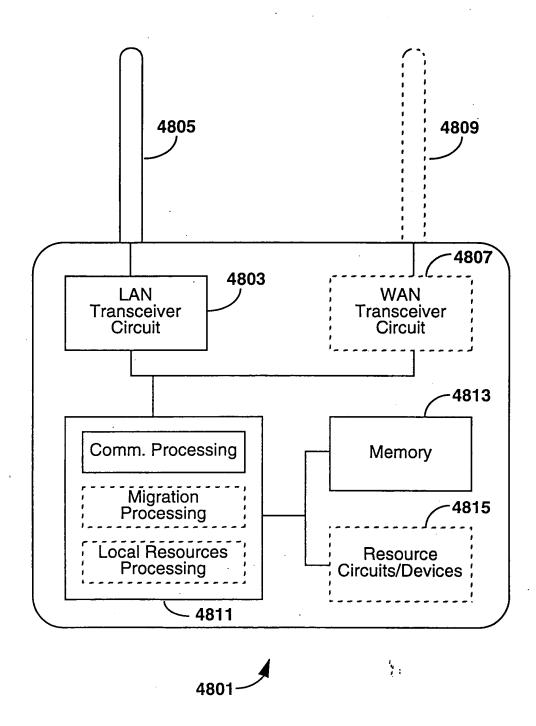


Fig. 48

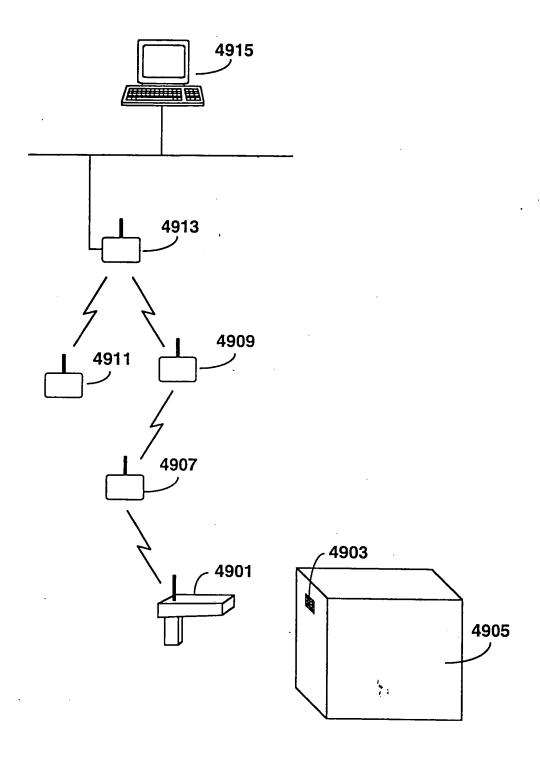


Fig. 49a

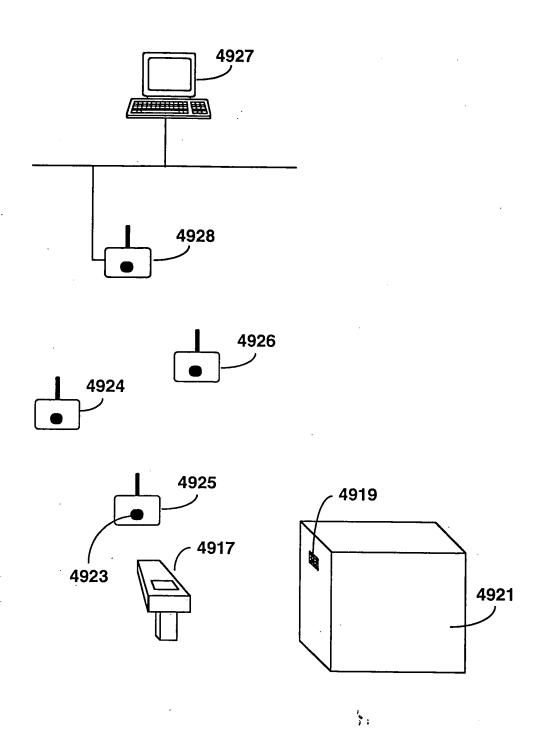


Fig. 49b

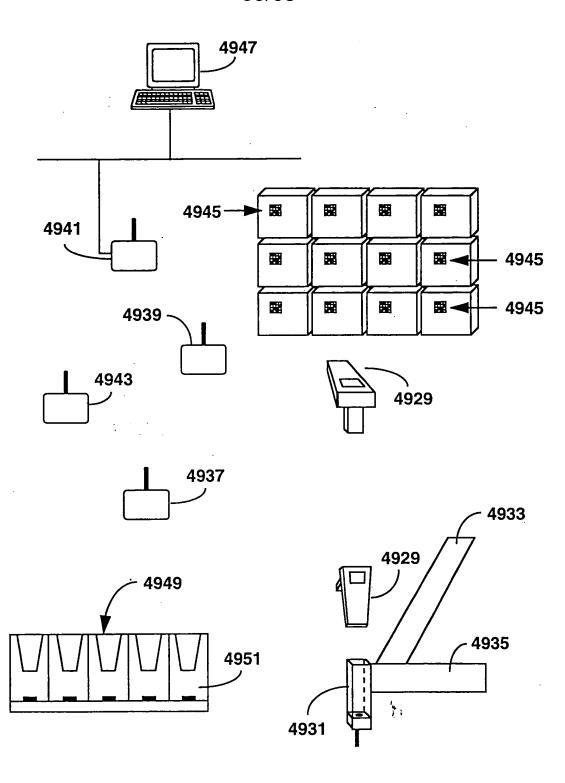


Fig. 49c

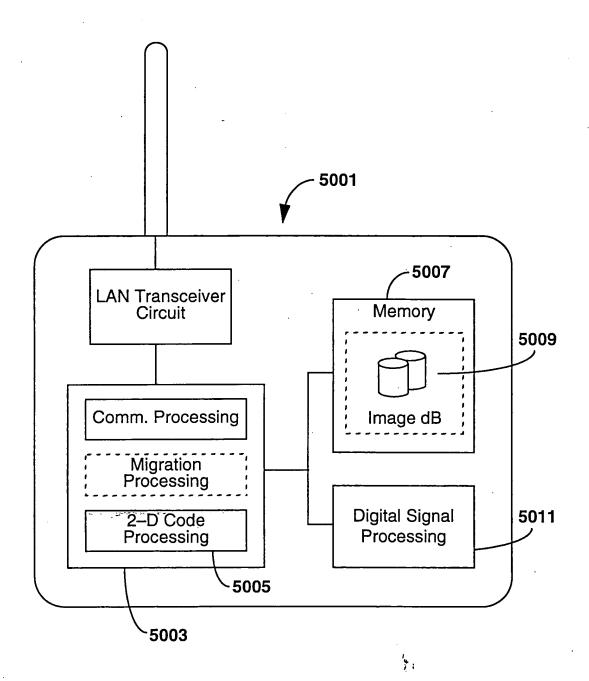


Fig. 50

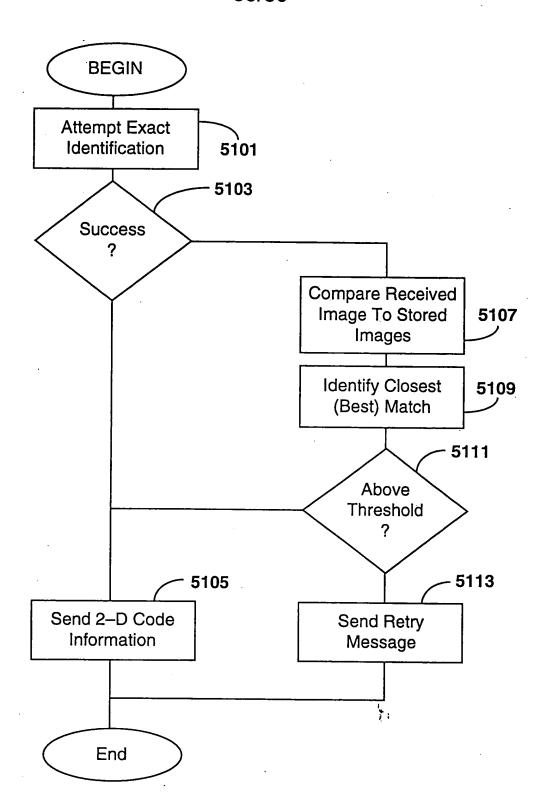


Fig. 51a

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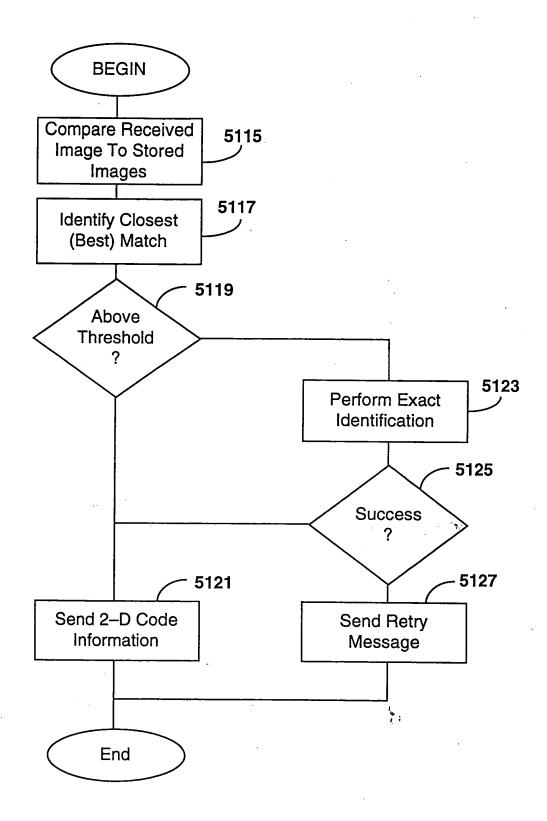
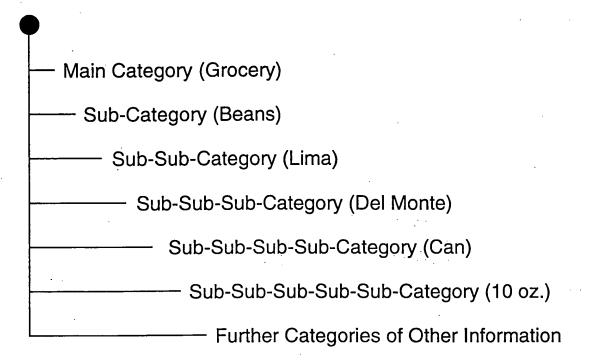


Fig. 51b



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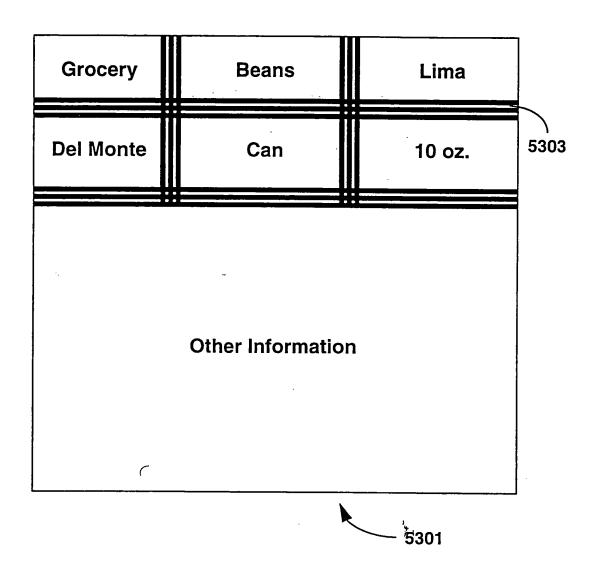


Fig. 53

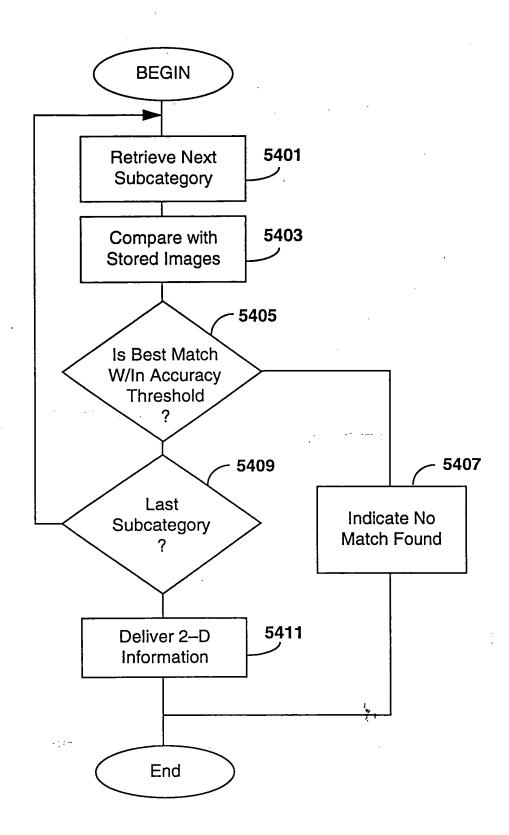


Fig. 54